



GNSS DSP Hardware Engineer

An exciting opportunity has arisen for a GNSS (Global Navigation Satellite Systems) DSP (Digital Signal Processing) Engineer to assist in the development of a family of GNSS receivers. The next stage of this development requires a DSP/GNSS design engineer to implement the real-time signal processing algorithms in hardware and assist on the development of RF and high-speed digital boards. You will take responsibility for the FPGA hardware design, development, test and specification of the electronic boards.

Applicants for this post must have:

- An Electronics or Computer Science first degree with a Masters/PhD in Digital Signal Processing/Electronics/Computer Science
- Industry experience in wide-band RF direct bandpass sampling receiver technology
- Experience of the use of the USRP1+WBX hardware platform for GNSS applications
- Experience of the following FPGA platforms: Xilinx Spartan3 and Spartan6, Altera Cyclone II/III and Actel IGLOO-M1
- Experience in software programming languages such as Matlab, Octave, C, Assembly
- Hardware programming languages such as Verilog or VHDL and the major vendors' FPGA design tools
- Industry experience in electronic hardware product development, in particular high-speed, real-time digital signal processing (hardware and software) and FPGA high-speed digital design is also required

Applicants should have:

- Excellent organisational and technical skills
- The ability to assess current and future hardware technologies
- Strong documentation skills
- Excellent communication skills

To apply, please send your CV and any covering information to: recruitment@nsl.eu.com

To find out more about us see www.nsl.eu.com